A MAJOR ATTEMPT to combine a compact CCD imager with its CMOS driving circuitry on a single chip was described in 1988. The sensor made use of a sophisticated clocking scheme. This could be seen as a first step towards a single-chip camera solution, the ultimate dream of every CMOS image sensor designer. This goal was later realized and presented for the first time at ISSCC 1998 by Smith et al. and Loinaz et al.