THE FORMATIVE YEARS: 1955-1960

1956 ADVANCE PROGRAM

THURSDAY, FEBRUARY 16
4:00 A.M. - REGISTRATION OPENS
Irvin Auditorium

9:30 A.M.-12:00 Noon - SESSION I - Irvine Auditorium

SPECIAL CIRCUITS
Chairman: P. L. Buskirk, University of PA.

9:30 A.M.-12:00 Noon - SESSION A

SYNTHESIS, COMPUTER METHODS

12:00 Noon-5:00 P.M. - LUNCHEON

3:00 P.M.-5:00 P.M. - Irvine Auditorium

Formal Opening of Conference
Gustave L. Husson, Chairman of Conference
Donald R. Pake, Chairman of Conference

3:30 P.M.-5:00 P.M. - SESSION B - Irvine Auditorium

SWITCHING CIRCUITS
Chairman: R. H. Thurston, Bell Telephone Laboratories, Inc.

3:45 P.M.-5:30 P.M. - SESSION B

TUTORIAL - University Museum
A9. "Linear Programming Circuits," R. H. Baker, Lincoln Laboratory, MIT.

5:00 P.M. - COCKTAIL BUFFET
Penn Sherrwood Hotel
7:00 P.M. - IMPERIAL GROUP DISCUSSIONS
Penn Sherrwood Hotel

FRIEDAY, FEBRUARY 17
8:30 A.M. - REGISTRATION OPENS
4:00 A.M.-12:00 Noon - SESSION III - Irvine Auditorium

DISPLAY ROOMS AND EXHIBITS
Chairmen: B. H. Marinus, Bell Telephone Laboratories, Inc.
B. R. Barbier, Bell Telephone Laboratories, Inc.

12:00 Noon-5:00 P.M. - LUNCHEON

3:00 P.M.-5:00 P.M. - Irvine Auditorium

Furnace Opening of Conference
Gustave L. Husson, Chairman of Conference
Donald R. Pake, Chairman of Conference

4:00 P.M.-5:00 P.M. - SESSION V - Irvine Auditorium

POWER AMPLIFIERS AND POWER SUPPLIES
Chairman: B. H. Marinus, Bell Telephone Laboratories, Inc.


5:45 P.M.-7:45 P.M. - SESSION V

TUTORIAL - University Museum
A12. "Linear Programming Circuits," R. H. Baker, Lincoln Laboratory, MIT.

7:00 P.M. - COCKTAIL BUFFET
Penn Sherrwood Hotel
9:00 P.M. - IMPERIAL GROUP DISCUSSIONS
Penn Sherrwood Hotel

1956 TRANSISTOR CIRCUITS CONFERENCE

SPONSORSHIP AND SCOPE

The 1956 Transistor Circuits Conference is sponsored jointly by the IRE Professional Group on Circuit Theory, the Science and Electronics Division of AIEE, and the University of Pennsylvania. The Conference is patterned after the previous ones held at the same location for the past several years and will include information on both linear and nonlinear transistor circuit applications. Emphasis will be on material not previously available in the literature and the program is designed to be of greatest value to engineers engaged in transistor circuit design and development. The Conference will also provide an opportunity to observe the behavior of transistor circuits. However, there is one day of tutorial papers scheduled for the purpose of orienting those who are new to this branch of science.

Conference rooms will be available at the Penn Sherrwood Hotel after the Cocktail-Buffet on Thursday in order to afford an opportunity to hold informal discussions of the type that were held last year.

Publication of many of the papers and professional journals is planned in lieu of Conference Transactions.

REGISTRATION

The Conference will be held on the campus of the University of Pennsylvania in the Irvine Auditorium and in the University Auditorium. Irvine Auditorium is located at the northeast corner of 34th and Spruce Street; the University Auditorium is just east of the southeast corner of 34th and Spruce Streets, Philadelphia, Pennsylvania. Accommodations are easily reached by streetcar routes 11 and 62, the 34th Street Station of the Pennsylvania Railroad is less than a mile from the University Campus.

Advance fees apply only to those registering by mail before February 1, 1956. Because of limited facilities and to assist the Local Arrangements Committee, advance registration and meal purchases are strongly recommended.

LOCATION

The Penn Sherrwood Hotel at 39th and Chestnut Streets has reserved a block of rooms for those attending the Conference. Lunches will be served in a limited number on both Thursday and Friday at Bostom Hall. For those who prefer to eat elsewhere, Philadelphia offers many fine restaurants.

The Cocktail-Buffet followed by the roundtable discussions will be held at the Penn Sherrwood Hotel on Thursday evening.

ACCOMMODATIONS