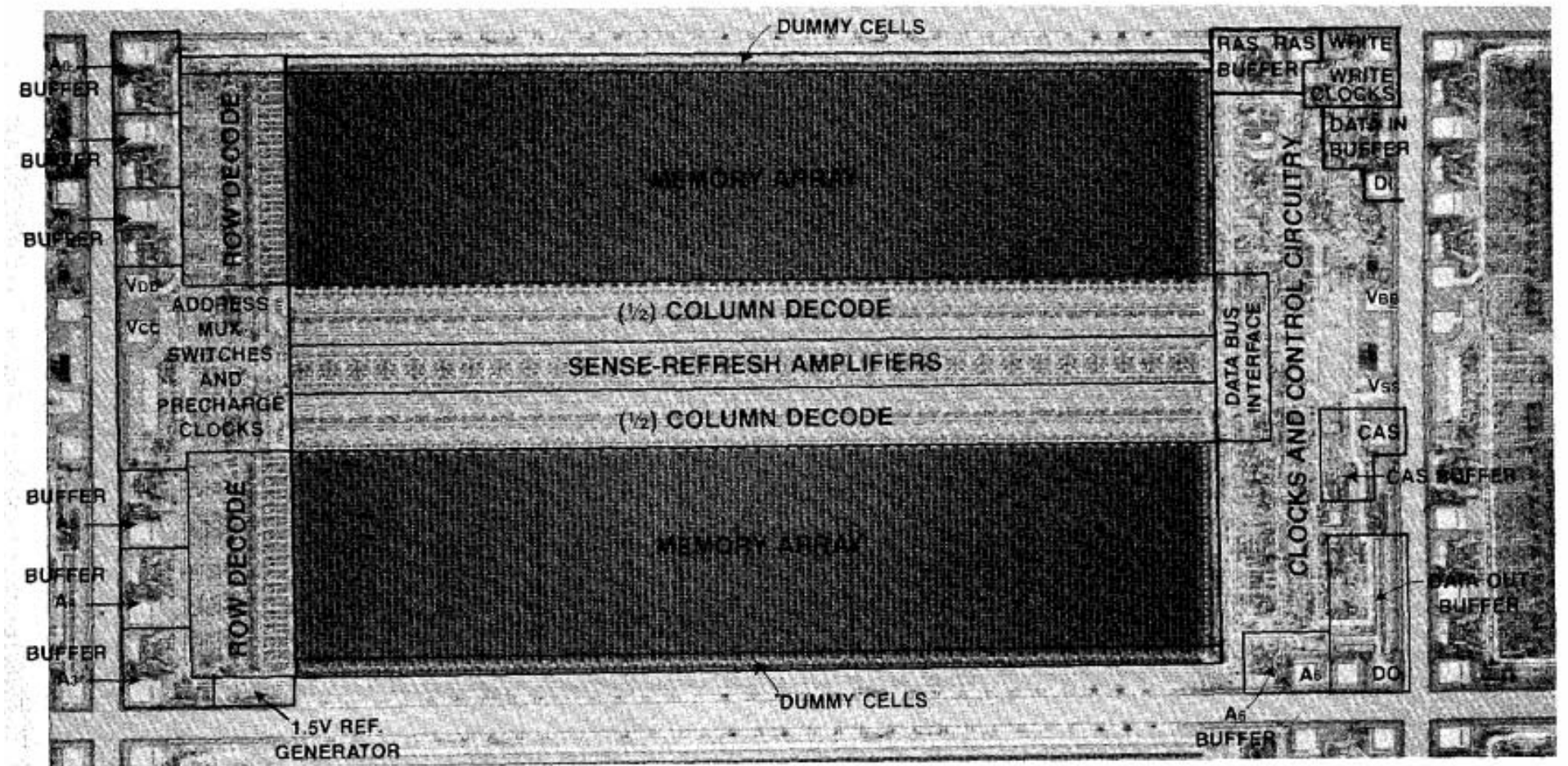


BIRTH OF MULTIPLEXED ADDRESSES

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A 16K x 1 Bit Dynamic RAM

SCHROEDER AND PROEBSTING (Mostek Corporation) described a 16K DRAM with “RAS bar” and “CAS bar” architecture. This became the industry standard DRAM architecture for future designs. This design used many innovative clocking techniques to improve performance and to reduce power. A cell size of 0.7 mil^2 , and the die size of $27.7\text{K} \text{ mil}^2$ was achieved using double poly NMOS technology.